



Pt. Dwarka Prasad Mishra  
Indian Institute of Information Technology,  
Design & Manufacturing, Jabalpur  
(An Institute of National Importance established by an Act of Parliament)

## **Admission Notice for Full Time Ph.D. Program**

under the "Visvesvaraya Ph.D. Scheme for Electronics and IT": Phase-II"  
Sponsored by MeitY, Govt. of India, for the AY 24-25

Online applications are invited from highly motivated research-oriented students for admission to the **Ph.D. programme (Full Time)** for the Academic Year 2024-2025.

- Full time Ph.D. Programme - 03 seats
- Part time Ph.D. Programme – 01 seat

under the "Visvesvaraya Ph.D. Scheme for Electronics and IT" in the field of **Electronic System Design and Manufacturing (ESDM) and IT/IT Enabled Services (ITES)**. List of research areas is attached as Annexure I.

The salient features of the scheme are as below:

➤ **For Full Time Ph.D. Programme**

- Monthly scholarship of Rs. 38,750/- for the first two years and Rs. 43,750/- for the rest 3 years (support up to 5 years)
- Rent reimbursement (As per the Government of India norms and subject to fulfilment of condition as per the scheme).
- Annual research contingency grant upto Rs.1,20,000/- per year.
- Financial support upto Rs.1.5 Lakh for attending International Conferences.
- Opportunity to visit labs abroad.

➤ **For Part Time Ph.D. Programme**

- Part-time Ph.D. seat is provisioned for professionals working at a reputed place/organization.
- The selected candidates should not be availing any Ph.D. Fellowship/scholarship from any other scheme of Central or State Government.
- Part-time candidates who do not have avail of any Ph.D. Fellowship/scholarship/stipend from any Central or State Government shall be provided a one-time incentive of Rs. 3 Lakh on successful completion of the Ph.D. degree. However, they are not eligible for monthly scholarships.

### **Eligibility for admissions**

A minimum of 60 percent marks OR a CPI/CGPA of 6.0 (on the scale of 10.00) shall be required at the Master's level qualifying examination (M.Tech./ M.E./ MCA) as the specified minimum for admission in a Ph.D. programme for the concerned disciplines. Please see the table below for the eligibility criteria for each discipline.

*Sudhakar*  
12/04/24

Ph.D. Program (Discipline)	Minimum Qualifications
Ph.D. in Computer Science and Engineering	M.E./M.Tech. with B.E./B.Tech./MCA/ M.Sc.in any discipline / branch. <b>Qualified GATE in Computer Science and Information Technology (CS)</b> or CSIR/UGC JRF/NET in Computer Science or Information Technology
Ph.D. in Electronics and Communication Engineering	M.E./M.Tech. with B.E./B.Tech./MCA/ M.Sc.in any discipline/branch. <b>Qualified GATE in Electrical Engineering (EE) or Electronics and Communication Engineering (EC) or Instrumentation (IN) or Computer Science and Information Technology (CS) or Physics (PH)</b>
Ph.D. in Smart Manufacturing	M.E./M.Tech. with B.E./B.Tech./MCA/ M.Sc. in any discipline/branch. <b>Qualified GATE in Electrical Engineering (EE) or Electronics and Communication Engineering (EC) or Instrumentation (IN) or Computer Science and Information Technology (CS) or Physics (PH) or Mechanical Engineering (ME) or related field.</b>

Above eligibility is minimum requirement only. Good academic record and higher qualification will be preferred, and mere eligibility shall not ensure a call for the admission test and/or viva voce.

All interested candidates, fulfilling the minimum eligibility criteria, must submit the application by Google Form and submission of the scanned copy of biodata in the attached format.

**Application Link :** [admission.iiitdmj.ac.in](http://admission.iiitdmj.ac.in)

#### Important Dates

- Start date of application – 12.04.2024 (Friday)
- Last date of application submission - 30.04.2024 (Tuesday)
- Announcement of list of shortlisted candidates - 10.05.2024 (Friday)
- Date of written test and/or interview – Between 27.05.2024 – 31.05.2024 (Monday – Friday)
- Announcement of list of selected candidates - 10.06.2024 (Monday)

#### Note:

The institute reserves the right to fix suitable criteria for short listing of eligible candidates satisfying qualification and experience. Only shortlisted candidates may be called for an interview.

#### For further queries contact at:

Email Address : [admission@iiitdmj.ac.in](mailto:admission@iiitdmj.ac.in) ; [aracad@iiitdmj.ac.in](mailto:aracad@iiitdmj.ac.in) or call at 0761-2794035, 6268371310.

*Virendra*  
12/04/24

Annexure I

**List of research areas for Visvesvaraya Ph.D. Scheme**

- Image Processing and Computer Vision
- Game Theory
- Machine and Deep Learning
- Software Engineering
- Natural Language Processing
- Computer Networks
- Wireless Networks
- Network and Cyber Security
- IoT
- Hardware Security
- Cloud Computing
- Clustering
- Social Network Analysis
- Computational Genomics & Proteomics
- RF and Microwave Engg
- Communication Engineering
- Signal and Image Processing
- Micro and Nano Electronics
- Power and Control System
- Robotics and Mechatronics
- Industrial IoT
- Augmented Reality / Virtual Reality

  
12/04/24

THE STATE OF TEXAS, COUNTY OF [illegible]

I, [illegible], County Clerk of said County, do hereby certify that

the within and foregoing

is a true and correct copy of the

minutes of the Board of Commissioners

of said County, held at the County Seat

on the [illegible] day of [illegible]

in the year of our Lord one thousand

eight hundred and [illegible]

and [illegible]

at [illegible]

County Clerk

[illegible]

By [illegible]

County Clerk

[illegible]

[illegible]

[illegible]

[illegible]

[illegible]

[illegible]

Attest my hand and the seal of said County

10-10-18